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TITLE: VIDEO CIRCUIT

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ABSTRACT:

PURPOSE: To display a picture data at a normal attitude even by a video software formed while being turned by 90°; by writing a digital picture data of a video signal into a memory and making the order of write different from the order of read from the memory by 90°;.

CONSTITUTION: An input video signal is separated into a luminance signal and a chrominance signal by a video processing circuit 4, converted into a time series serial signal by a multiplexer 5, digitized and written in a buffer memory 7. A picture data read from the memory 7 is written in an odd number field memory 9 by a field switching circuit 8. In this case, the signal is written longitudinally from the right to the left. In case of reading a signal from the memory 9, it is read sequentially from a direction different from the write direction by 90°, that is, from the left to the right and from the upper to the lower direction. Thus, in case of inputting a BCG signal obtained from a field switching circuit 11, D/A converters 12~14 and a matrix 15 to

the CRT, a pattern turned right by 90&deg; is reproduced. The processing above is applied entirely similarly as to an even number of fields.

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